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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: John W. Rapp, Larry Jackson, Mark Jones,
Troy Cherasaro

Title: PROGRAMMABLE CIRCUIT AND RELATED
COMPUTING MACHINE AND METHOD

Serial Number: 10/684,057

Filing Date: October 9, 2003

Examiner/Unit: Richard B. Franklin/2818

Attorney Docket No.: 1934-14-3

CERTIFICATE OF MAILING OR TRANSMISSION

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In compliance with the duty of disclosure under 37 CFR § 1.56, Applicant submits herewith patents, publications, or other information for consideration during the examination of this application.

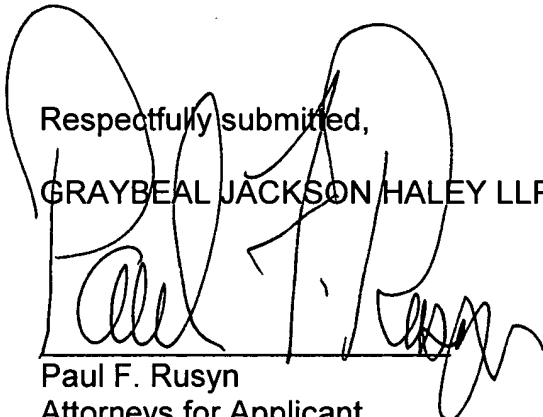
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Under 37 C.F.R. § 1.97(e)(1), the undersigned hereby states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure.

No additional costs are believed to be due in connection with the filing of this Supplemental Information Disclosure Statement. However, please charge any necessary fees in connection with the enclosed statement to our Deposit Order Account No. 07-1897.

Respectfully submitted,

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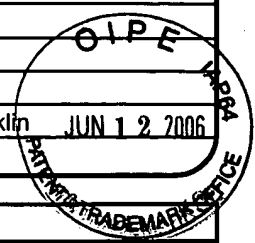
INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/684,057
Filing Date	October 9, 2003
First Named Inventor	John W. Rapp
Art Unit	2818
Examiner Name	Richard B. Franklin
Attorney Docket Number	1934-14-3



U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

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Sheet 2 of 2

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First Named Inventor	John W. Rapp
Art Unit	2818
Examiner Name	Richard B. Franklin
Attorney Docket Number	1934-14-3

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1996, pp. 98, 101-103	
		International Search Report for PCT/US 03/34556, March 7, 2006	

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